Verification Student – PHY team

Be a part in a small team developing the LTE PHY of Altair's cutting edge IOT products

Responsibilities:
Mastering complex designs (DUT) and applications in the field of communications in close collaboration with HW team.
Developing verification test plans, tools, test benches and environments, from block-level to chip-level.
Running and debugging the verified designs and acquiring the required knowledge in the field of communications.

Requirements:
3rd year leading university Electric Engineering or Computer Engineering student
Hands-on VLSI design or verification experience – an advantage
Experience in SW design and programming – an advantage
Background in communication or DSP – an advantage

Personal skills:
Team player and top performer – must submit grade sheet
Thinker, self-learning, curious and motivated

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