Intern HW Verification Engineer #15600  Apply Now

Job Description

HW Verification Intern

As a Verification Intern, you will be a Hw verification student in the Design group of Arm Security IP Development center in Ra’anana, Israel. We are looking for a talented, innovative student for our verification team that designs and implements sophisticated verification environments for IP cores for Arm system security products, serving the Mobile, Automotive and IoT markets. You will help Arm maintain and strengthen its leadership in the embedded world. If you are up to the task, we want to hear from you!

Job Purpose & Accountabilities:

- Implementation of units and parts of verification environment including planning, testing and debugging.
- Executing of our products verification plan tasks (System Verilog UVM), such as random and direct tests, coverage and assertions.
- Working closely with the Product, Architecture and Design teams to deliver the product.

Job Requirements

- Pursuing a B.Sc\M.Sc. in Electrical Engineering, Computer Engineering, from a leading university - Students who completed at least 3 semesters
- Good system view capabilities and debug skills.

Personal Attributes

- Creative, dynamic thinker, open minded with strong problem-solving skills
- Fast learner of new technologies and standards
- Versatile and ready to work in a dynamic and challenging working environment
- Team player with multi-tasking ability and good interpersonal skills
- Good written and spoken English communication

Desirable (Optional)

- Previous demonstrated project work in Verification, VLSI – advantage
- Experience with C++, C, Tcl, Verilog, Unix environment including scripting (Perl, C-shell) is beneficial