**Experienced Logic Design Engineer – Tel Aviv**

IBM Hardware Development Lab is leading the ongoing effort of Processors Development, including embedded processors, servers and super computers. Team is working with state of the art technology and high quality design to keep up with the growing complexity of processors.

We are hiring an **experienced Logic Design Engineer** and you can join this team effort. Role includes, but is not limited to
- Digital design of a wide variety of logic functions
- Extensive simulation testing and/or characterization of designs
- Static timing analysis and closure
- Working as part of a local and/or global design team.

**Requirements**
- B.Sc in Electrical Engineering or Computer Science/Engineering
- At least 3 years of experience in logic design
- Proven experience with VHDL or Verilog
- Excellent technical capabilities and understanding of complex technical challenges
- Excellent communication and interpersonal skills
- Team player
- Motivated, self-directed and able to work independently
- Fluent in English & Hebrew

**Advantage**
- Understanding of computer micro architectures
- Knowledge and/or experience in Python programming
- Knowledge in communication, IO, memory, and processors
- Familiarity with verification methodologies

Location: Tel Aviv

Apply through link - [ibm.biz/Logic_Design_TLV](http://ibm.biz/Logic_Design_TLV)