IBM Hardware Development Lab is leading the ongoing effort of Processors Development, including embedded processors, servers and super computers. Team is working with state of the art technology and high quality design to keep up with the growing complexity of processors. We are hiring an experienced Hardware Verification Engineer and you can join this team effort.

You will be involved in planning and executing tests in a large scale and complex verification environment, in addition to customizing parts of the environment via coding in C++ and script languages. Work will be performed as part of a local and/or global verification team.

**Requirements**
- B.Sc in Electrical Engineering or Computer Science/Engineering
- At least 2 years of experience in VLSI verification
- Excellent technical capabilities and understanding of complex technical challenges
- Excellent communication and interpersonal skills
- Team player
- Motivated, self-directed and able to work independently
- Fluent in English & Hebrew

**Advantage**
- Understanding of computer architectures
- Knowledge and/or experience in object-oriented programming (C++)
- Knowledge in communication, IO, memory, and processors
- Familiarity with verification methodology

Location: Tel Aviv

Apply through link - [ibm.biz/Senior_Verification_TLV](http://ibm.biz/Senior_Verification_TLV)