IBM’s Hardware Development Lab is leading the ongoing effort of Processors Development, including embedded processors, servers and super computers. Team is working with state of the art technology and high-quality design to keep up with the growing complexity of processors. We are hiring an **experienced Array Circuit Design Engineer** and you can join this team effort.

You will participate in the physical implementation of the next IBM generation of CPU's, designed with IBM’s latest Silicon process, with emphasis on high performance (low power) memory design, Using cutting edge tools and design methodology. Due to the design complexity and tight requirements, the person who will fill this position will require fundamental VLSI and digital circuit design understanding and know how.

Role includes but is not limited to
- Owning the physical design of SRAM array from initial design planning until tape-out. Including architecture definition, schematic, electrical simulations, physical implementation, reliability, and more.
- Working closely with the Logic design team, Layout team and integration team.
- Working in the frame of a world global design team.

**Requirements**
- B.Sc or M.Sc in Electrical Engineering.
- At least 2 years of relevant experience
- VLSI & digital circuit design knowledge
- Acquaintance with digital CAD tools and Linux like work environment
- Team player
- Motivated, self-directed and able to work independently
- Fluent in English and Hebrew

**Advantages**
- Scripting (Shell/SKILL/PERL/TCL)
- Experience at Custom circuit design / synthesis
- RTL coding
- Processor's architecture
- Deep submicron technologies and basic device physics

**Location - Tel Aviv (Azrieli) or Haifa (University Campus)**

**Apply through the link** - [ibm.biz/Arrays_Circuit_Design](http://ibm.biz/Arrays_Circuit_Design)