IBM's Hardware Development Lab is leading the ongoing effort of Processors Development, including embedded processors, servers and super computers. Team is working with state of the art technology and high quality design to keep up with the growing complexity of processors. We are looking for a talented and motivated STUDENT to join this team effort.

You will participate in the physical implementation of the next IBM generation of CPU's, designed with IBM's latest and greatest Silicon process, while using cutting edge tools and design methodology, Synthesis. Role include, but is not limited to -
- Being part of a team owning the physical design of a set of blocks from initial design phase to RIT
- Bring design blocks from RTL through automated synthesis process to create the Boolean equivalent physical implementation
- Working as a member of a design team
- Working closely with the Logic design team

Requirements
- 3rd year B.Sc student/M.Sc student of Electrical Engineering, with at least 1.5 years till graduation (graduation in 2020 or later).

Please attach your Grade sheet so we may process your application
- Available for at least 2 days a week.
- Good Electrical Engineering Fundamentals Knowledge
- Team player
- Motivated, self-directed and able to work independently
- Fluent in English and Hebrew

Advantages
- Scripting (Shell/SKILL/PERL/TCL)
- RTL coding
- Processor's architecture
- Deep submicron technologies and basic device physics
- Custom design
- RTL, Digital Circuit Design Courses
- Master's Degree Student

Location: Haifa / Tel Aviv
Start date - 1/1/2019

Apply through the link >> [ibm.biz/CircuitEngineer_Student]