As a Staff Verification Engineer, you will be a verification technical leader in the Design group of arm Security IP Development center in Ra'anana, Israel. We are looking for you, the experienced engineer, for our verification team that designs and implements sophisticated verification environments for IP cores for Arm system security products, serving the Mobile, Automotive and IoT markets. You will help Arm maintain and strengthen its leadership in the embedded world. Are you up to the task? If the answer is yes, we will be happy to hear from you.

**Job Purpose & Accountabilities:**

- Planning and writing Verification plans and test-plans.
- Ownership of verification environment blocks including planning, implementing, testing and debugging.
- Blocks logic design (optional).
- Working closely with the Product, Architecture and Design teams to deliver the product.
- Taking part in the verification team tasks (system Verilog).
Job Requirements:

- B.Sc. in Electrical Engineering, Computer Engineering, Computer Science/Software Engineering
- 7+ years of experience in HW Verification developing, methodologies and implementation.
- Logic Design experience – an advantage
- Verification team leading – an advantage
- Experience and understanding of Random Verification concepts, methodologies and Environments
- Wide knowledge of Coverage driven verification techniques
- Experience in System Verilog and System Verilog UVM.
- Verilog development of stubs and test-benches
- Experience in Unix environment including scripting (Perl, C-shell)
- Excellent system view capabilities and debug skills.

Personal Attributes

- Creative, dynamic thinker, appreciative with strong analytical skills.
- Self-starter of new technologies and standards.
- Versatile and ready to work in a dynamic and significant working environment.
- Teammate with multi-tasking ability and good social skills.
- Good written and spoken English communication.

For more information or to apply, please visit arm careers: tal.rozenblum@arm.com