Associate Design Verification engineer (Student position) - Petah Tikva

Marvell Israel is searching for highly potential and motivated student to join to the professional DFT (Design for Test) team that is addressing hi-scale next generation projects. Will be asked for advanced design and verification related activities while working with the most advanced commercial Electronic Design Automation (EDA) vendors and Tools for meeting Marvell hi-end standards. Asking for high technical skills and innovative thinking, when working on advance and complex top of technology devices, infrastructure, flows and tools.

**Qualifications:**

2nd / 3rd year student in EE (must)
- Excellent interpersonal and communication skills (must)
- Self-managed, Independent work capabilities and good learning skills (must)
- Innovative thinking (must)
- Highly technical and analytical capabilities (must)
- Knowledge with logic design and verification (advantage)
- Knowledge in programming languages Verilog, VHDL, C/C++, TCL (advantage)
- Experience with workflow and automation infrastructure development (advantage)
- Experience with commercial Electronic Design Automation (EDA) vendors and Tools (advantage)