Architectural Exploration and Modeling Intern - Petah Tikva

The intern will develop simulation models at device level and system level based on tools like C++ and SystemC. The models will be used for architecture exploration. The definition might be in different levels of details – either as a fully detailed specification that needs implementation or at higher level problem statement that requires the candidate to develop a solution.

In any case – it is expected from the candidate to fully understand the architecture being explored and the results of the simulation in order to fix/improve the solution.

Qualifications:

Education: 3rd year student for BSC in electrical or computer engineering

Skills:

- Programming – Advantage
  - C/C++, SystemC, Perl, Python scripting
- Knowledge - Advantage
  - Computer architecture
  - Networking