Job ID: JR0001870
Job Category: Intern/Student
Primary Location: Petach-Tiqwa, IL
Other Locations:
Job Type: Intern

Phy System Engineering Student

Job Description

The WiFi Core Development department is looking for a student to its Phy System Engineering Team. The role requires a wide system learning and understanding of all the RF and Phy aspects of Intel’s wireless communication products. The role includes development and integration of advanced radio calibrations and digital compensation algorithms, accompanying the integration process and making sure the design meets the system requirements, as well as solving complex multi-disciplinary system issues involving the HW/SW/RF/Modem domains in close cooperation with all of the development teams. As part of the role you will be required to run system tests, analyze data and optimize performance based on the lab findings. This is a unique opportunity to combine M.Sc studies in EE alongside with earning practical experience in system engineering of cutting edge wireless technologies and deep understanding of design, development, integration and manufacturing processes.

Qualifications

M.Sc student in Electrical or Computer Engineering. Availability to work at least 3.5 days a week.

DSP/communication courses (Signal processing, Random Signals, Digital communication or Analog communication).

Independent and self-motivated, capable of executing in an environment of uncertainty. Experience in engineering work (system, HW or SW) - Advantage. Familiarity with RF/HW lab work - Advantage.

Inside this Business Group

The Platform Engineering Group (PEG) is responsible for the design, development, and production of system-on-a-chip (SoC) products that go into Intel’s next generation client and mobile platforms. PEG strives to lead the industry moving forward through product innovation and world class engineering.