Plan and control tool for chip and IP verification
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- Research Title: Plan and control tool for chip and IP verification

- Project Schedule
  - Two semesters

- Suggested format: Undergraduate project

- Site: PTK
Plan and control tool for chip and IP verification

Background:

• Verification of Chip and IP is both effort and time consuming. A database oriented tool is needed to effectively plan the verification activities as well as track them during the execution phase.

• Currently there is an Excel-based tool for that purpose. The Excel tool does not hold a database that enables trend tracking. Moreover, the Excel tool has to be specifically configured for each chip or IP.

Goals of this project:

• Create a flexible database oriented tool.

• The tool to be connected to a BI (Business Intelligence) system to enable
  – Planning of direct tests, random testing and other verification methods
  – Collecting up to date status from various verification tools and/or reports
  – Easy generation of up to date status including trends
  – The database will enable taking a weekly snapshot at the single test level
Academic Prerequisite Requirements

- Background in Database creation and management
- Knowledge in a BI software (e.g. QlikView)
- SQL
- VBA
Example – direct testing trend – plan vs. actual by milestone